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# **Eurospan Baffle Integrated Lighting**

Conwed has partnered with Kelvix to supply the lighting system for this product. All documentation is directly from the manufacturer.



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## ULV36 36 WATT-24 VOLT | CLASS 2 SUPPLY

Fixture Type:	
Project:	

Location: \_

#### **PRODUCT FEATURES**

- 431 Hz/Flicker-free Dimming Down to 5%
- Incandescent, ELV, MLV, or 0-10 V Dimming
- Protections: Short Circuit/Over Current/Over Voltage
- Free Air Convection Cooling
- Suitable for Dry/Damp Location
- UL-Listed Class 2

#### SPECIFICATIONS

Model	ULV36
Input Voltage	100-277 VAC
Output Voltage	24 VDC / Constant Voltage
Max. Wattage	36 W
Temp Range	-20°F–158°F
Dimensions $W \times H \times D$	5.906" × 2.52" × 1.417"
Classification	Class 2





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## ULV36 36 WATT-24 VOLT | CLASS 2 SUPPLY



**MODEL LIST** 

Model Name	Rated Input Voltage	Rated Output Power	Rated Output Voltage	Output Current	Note
	120-277 VAC				
ULV36	120 VAC (Phase Cut Dimming)	36 W	24 VDC	0-1500 mA × 2	

## **SPECIFICATION**

Parameters	Symbols	Test Conditions/Comment	Min	Тур	Max	Units
INPUT						
Input Voltage	VIN		108		305	VAC
Rated Input Voltage	VIN RATED	Phase Cut Dimming		120		VAC
	No Phase Cut Dimming	120		277	VAC	
Input Frequency	fline		47	50/60	63	Hz
Input Current I <sub>IN</sub>	Full Load, $V_{IN} = 120 VAC$			0.40	Α	
	Full Load, $V_{IN} = 230 VAC$			0.20	Α	
		Full Load, VIN = 277 VAC			0.18	А
GENERAL CHARACTE	RISTICS					
	$30\% - 100\%$ Load, $V_{IN} = 120$ VAC	0.95			PF	
Power Factor	PF	$50\% - 100\%$ Load, $V_{IN} = 230$ VAC	0.9			PF
	$70\% - 100\%$ Load, $V_{IN} = 277$ VAC	0.9			PF	
	$30\% - 100\%$ Load, $V_{IN} = 120$ VAC			20	%	
Total Harmonic Distortion	THD	$50\% - 100\%$ Load, $V_{IN} = 230$ VAC			20	%
Distoluon	70%-100% Load, V <sub>IN</sub> = 277 VAC			20	%	
	Full Load, $V_{IN} = 120 VAC$	82	83		%	
Efficiency	Efficiency ŋ	Full Load, VIN = 230 VAC	83	83.5		%
-	Full Load, $V_{IN} = 277 VAC$	83	83.5		%	
Turn On Delay Time	T <sub>on_delay</sub>	Cold Start, No Dimmer		0.3	0.5	S
Leakage Current	Leakage	$V_{IN} = 277 VAC / 60Hz$			0.5	mA
OUTPUT						
Output Voltage	Vout	No Dimming	22.8	24	24.7	V
Output Current	lout		0		1500	mA
Line Regulation	IOUT-LINE				1	%
Load Regulation	IOUT-LOAD				1	%
Ripple Voltage	IOUT-RIPPLE	Full Load, (pk-to-pk)/( $2 \times Average$ )			10	%
Output Voltage Overshoot	IOVERSHOOT	Power ON			5	%
0-10V OR RESISTOR	DIMMING					
voltage source (0-10)	/DC) or external resist				х <i>У</i>	
5% VOUT.	100 % Vout to 5 % Vo	UT. When V <sub>DIM</sub> is 8-10 VDC, the output voltage n	naintains 100% V <sub>OUT</sub>	, and when V <sub>DIM</sub> is t	pelow 0.6 V, the outpu	ut voltage is
Absolute Maximum Voltage on 0-10 V Pin	VDIM		-2		15	V
Source Current on 0-10V Dimming Pin	Idim			100		uA
V <sub>DIM</sub> Voltage for Full Bright	VDIM-MAX		8			V
Output Duty Cycle	D0-10V	PWM Output	5		100	%

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## ULV36 36 WATT-24 VOLT | CLASS 2 SUPPLY



## **SPECIFICATION (CONT.)**

The dimming range is 100 when the duty cycle below PWM Frequency High Level Voltage of PWM Signal Lower Level Voltage of PWM Signal Output Duty Cycle TRIAC DIMMING The unit is compatible wit Input Voltage	0 % Vout to 5 % Vout 6 %. fPWM VPWM-High VPWM-Low DPWM th leading-edge and VIN-TRIAC DIM	at can be used to dim the output voltage via the du put. When the duty cycle is 80 % to 100 %, the out PWM Output d trailing-edge dimmer.			1 12 1	ntains 5 % V <sub>OUT</sub> KHz V V
The dimming range is 100 when the duty cycle below PWM Frequency High Level Voltage of PWM Signal Lower Level Voltage of PWM Signal Output Duty Cycle FRIAC DIMMING The unit is compatible wit Input Voltage	0 % Vout to 5 % Vout 6 %. fPWM VPWM-High VPWM-Low DPWM th leading-edge and VIN-TRIAC DIM	DUT. When the duty cycle is 80 % to 100 %, the out	0.1 0.1 0	100 % V <sub>OUT</sub> , and the	1 12 1	KHz
vhen the duty cycle belov PWM Frequency High Level Voltage of PWM Signal Lower Level Voltage of PWM Signal Output Duty Cycle <b>IRIAC DIMMING</b> The unit is compatible wit Input Voltage	w 6 %. fpwm Vpwm-High Vpwm-Low Dpwm th leading-edge and Vin-TRIAC DIM	PWM Output	0.1 8 0		1 12 1	KHz
High Level Voltage of PWM Signal Lower Level Voltage of PWM Signal Output Duty Cycle FRIAC DIMMING The unit is compatible wit Input Voltage	VPWM-High VPWM-Low DPWM th leading-edge and VIN-TRIAC DIM	· · · ·	8 0	10	12	V
of PWM Signal       Lower Level       Voltage of       PWM Signal       Output Duty Cycle       TRIAC DIMMING       The unit is compatible with       Input Voltage	VPWM-Low DPWM th leading-edge and VIN-TRIAC DIM	· · · ·	0	10	1	
Voltage of PWM Signal Output Duty Cycle IRIAC DIMMING The unit is compatible wit Input Voltage	D <sub>PWM</sub> th leading-edge and VIN-TRIAC DIM	· · · ·				v
RIAC DIMMING The unit is compatible wit Input Voltage	th leading-edge and VIN-TRIAC DIM	· · · ·	5		100	
The unit is compatible wit Input Voltage	VIN-TRIAC DIM	d trailing-edge dimmer.			100	%
Input Voltage	VIN-TRIAC DIM	d trailing-edge dimmer.				
1 0						
	Птрило			120		VAC
Output Duty Cycle	DTRIAC	PWM Output	0	-	100	%
Suggest Load Range	PSuggest	$V_{IN} = 120 \text{ VAC}$	5		36	w
ROTECTION						
Over Voltage Protection	Vovp	Latch Off Mode			30	v
Over Temperature Protection	Тотр	If the case temperature exceeds OTP point, the output voltage of the driver is automatically reduced.	100	105	110	°C
Short Circuit Protection		It will recover automatically	/ after fault conditio	ns is removed.		
INVIRONMENT						
Storage Temperature	TStorage	Humidity: 5 % RH to 95 % RH	-40	-	+85	°C
Operating Relative Humidity	Ha	Non Condensing	10		90	%
DTHERS						
Life Time	TLife	Full Load, 120 VAC Input, 50 °C	50			kHrs
MTBF	TMTBF	Case Temperature	200			kHrs
Dimension $L \times W \times H$		5.906"×2.52"×1.417	7" (150mm × 64mr	n × 36mm)		
AFETY COMPLIANCE						
UL Listed		UL8750 Compliance to UL1	310 Class 2, CSA-0	22.2 No. 107.1		
MC COMPLIANCE						
FCC Part 15B		Conducted Emission Te	est and Radiated En	iission Test		

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# ULV36

36 WATT-24 VOLT | CLASS 2 SUPPLY



## **TYPICAL APPLICATION**

#### 0-10V Dimming (120-277V)



## DMX Control (120-277V)



## Primary Side Switching (120-277V)



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